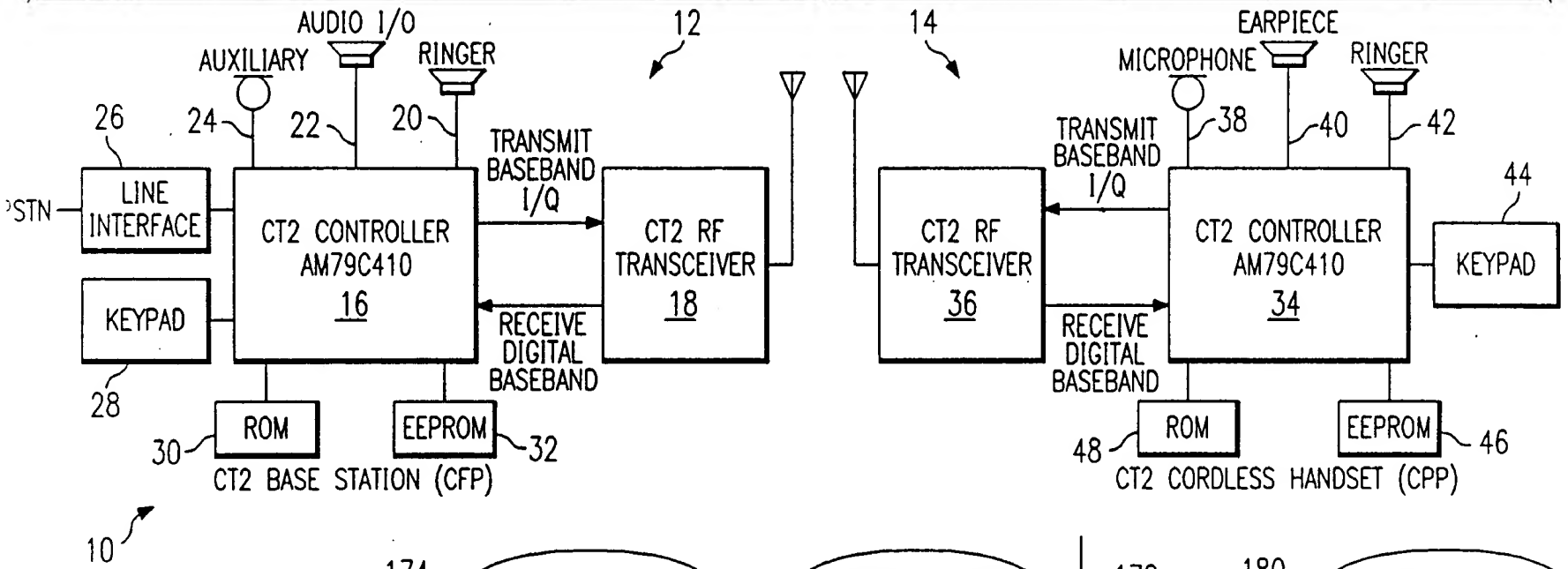
FIG. 1



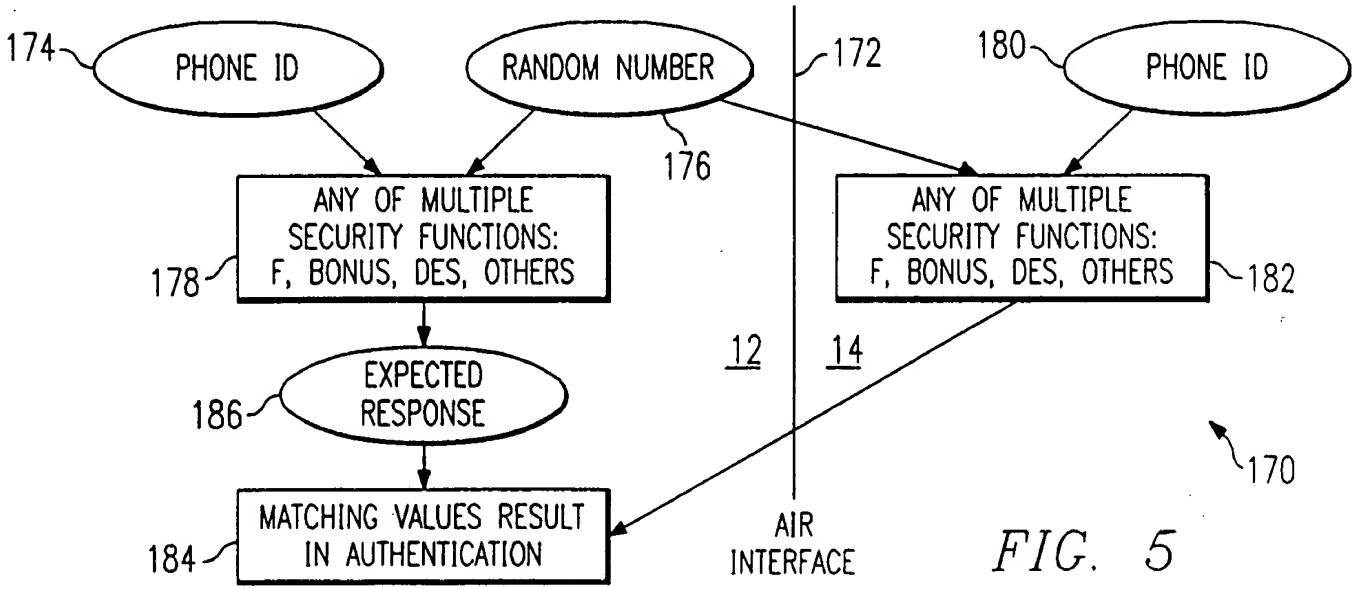


FIG. 5

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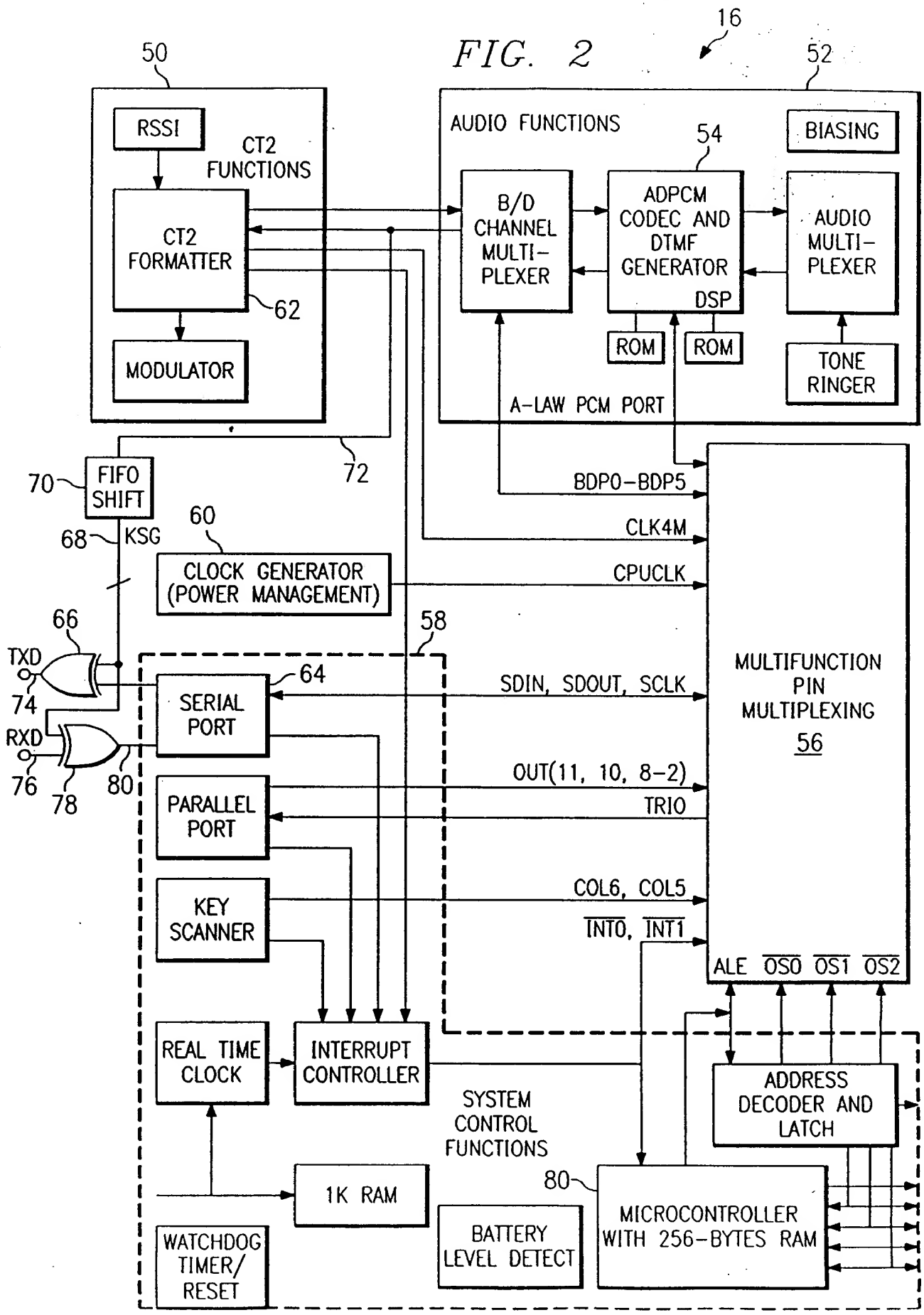
08/228717

FIG. 2


16
50
52
RSSI
CT2 FUNCTIONS
CT2 FORMATTER
62
MODULATOR
AUDIO FUNCTIONS
54
BIASING
B/D CHANNEL MULTI-PLEXER
ADPCM CODEC AND DTMF GENERATOR
DSP
AUDIO MULTI-PLEXER
ROM
ROM
TONE RINGER
A-LAW PCM PORT
70
FIFO SHIFT
72
68
KSG
60
CLOCK GENERATOR (POWER MANAGEMENT)
BDP0-BDP5
CLK4M
CPUCLK
66
58
TXD
74
RXD
76
78
80
64
SERIAL PORT
PARALLEL PORT
KEY SCANNER
SDIN, SDOUT, SCLK
OUT(11, 10, 8-2)
TRIO
COL6, COL5
INT0, INT1
MULTIFUNCTION PIN MULTIPLEXING
56
ALE OS0 OS1 OS2
REAL TIME CLOCK
INTERRUPT CONTROLLER
SYSTEM CONTROL FUNCTIONS
ADDRESS DECODER AND LATCH
1K RAM
80
MICROCONTROLLER WITH 256-BYTES RAM
BATTERY LEVEL DETECT
WATCHDOG TIMER/ RESET

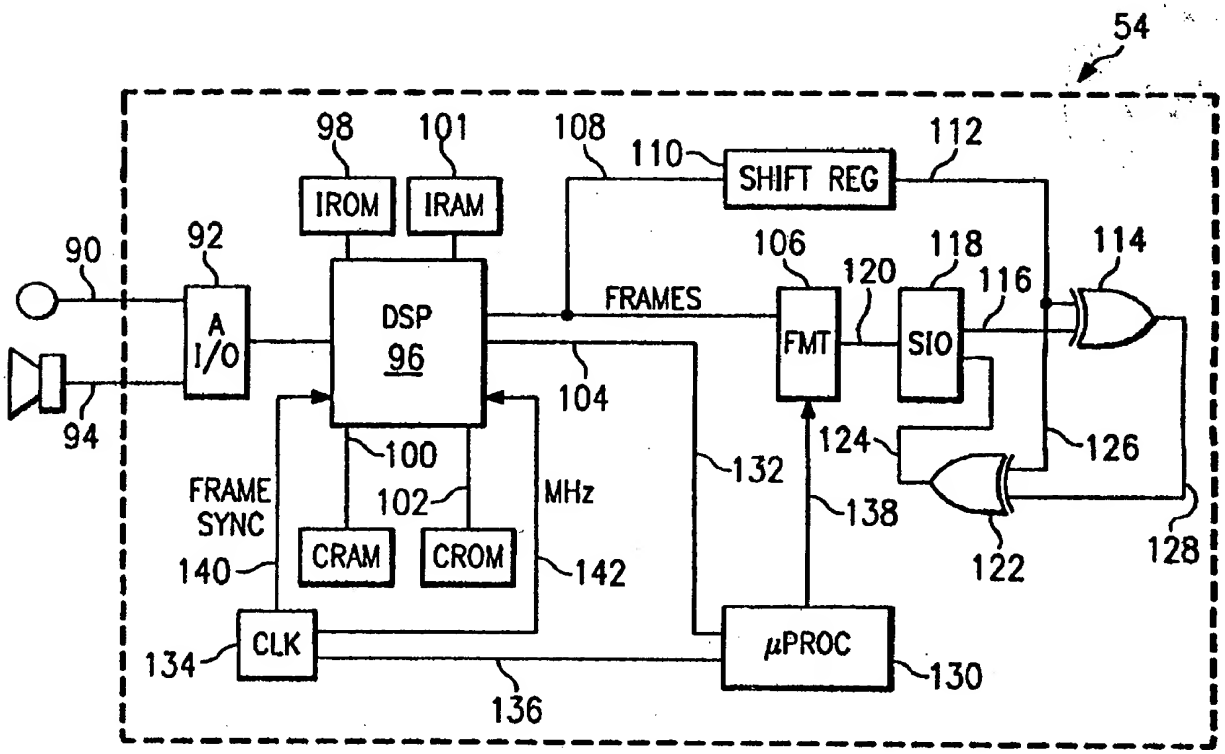

54
98
101
108
110
SHIFT REG
112
IROM
IRAM
90
92
106
118
114
120
116
A
I/O
DSP
96
FRAMES
FMT
SIO
104
94
124
126
100
132
FRAME
SYNC
102
MHz
138
140
CRAM
CROM
142
122
128
134
CLK
μPROC
130
136

FIG. 3

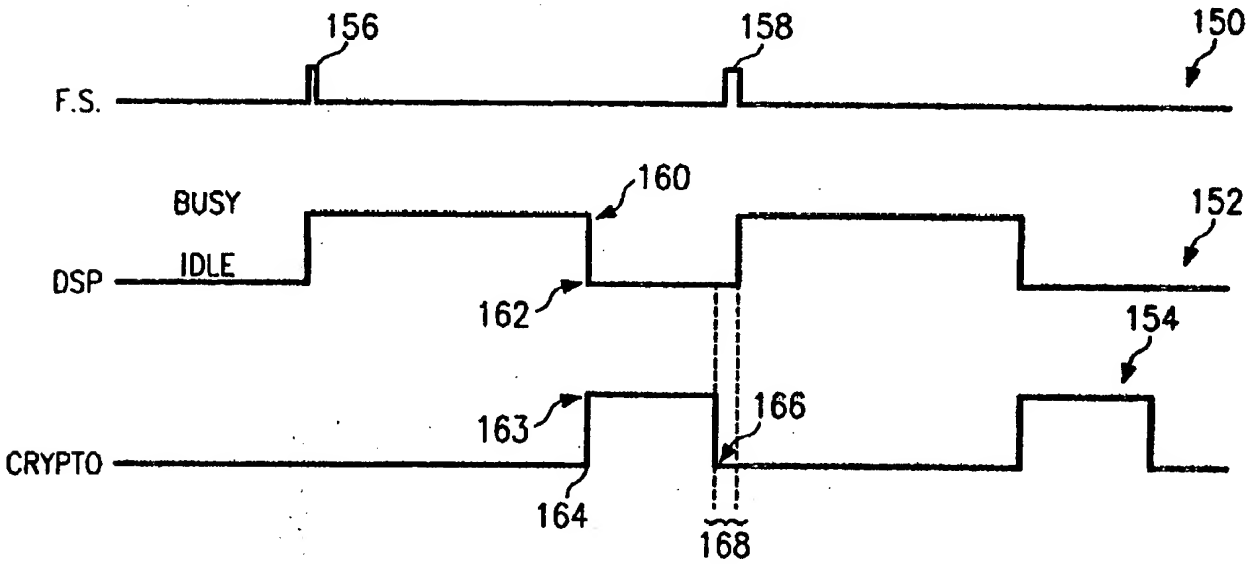

156
158
150
F.S.
BUSY
160
152
DSP
IDLE
162
154
163
166
CRYPTO
164
168

FIG. 4

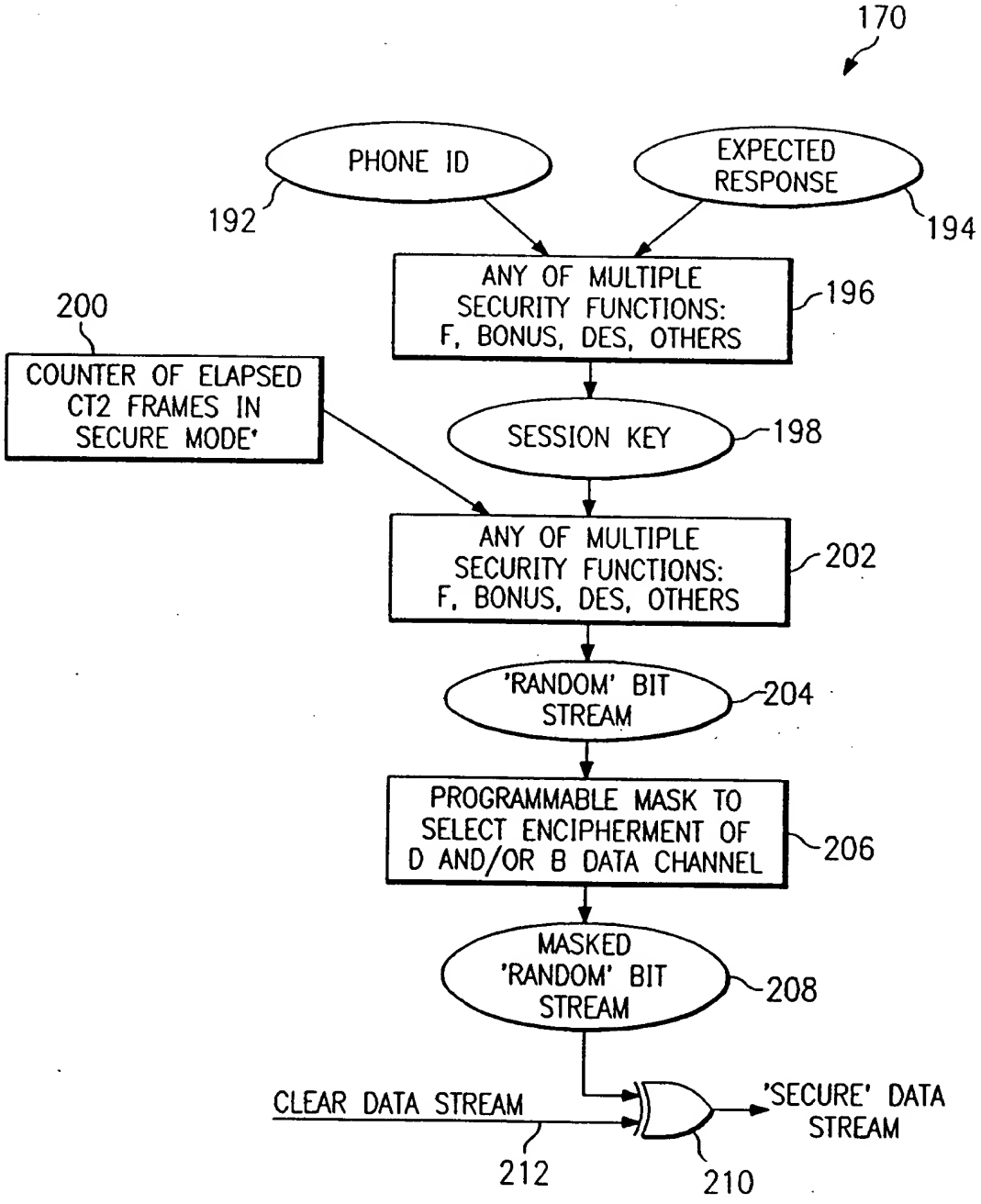

170
PHONE ID
192
EXPECTED RESPONSE
194
ANY OF MULTIPLE SECURITY FUNCTIONS: F, BONUS, DES, OTHERS
196
200
COUNTER OF ELAPSED CT2 FRAMES IN SECURE MODE'
SESSION KEY
198
ANY OF MULTIPLE SECURITY FUNCTIONS: F, BONUS, DES, OTHERS
202
'RANDOM' BIT STREAM
204
PROGRAMMABLE MASK TO SELECT ENCIPHERMENT OF D AND/OR B DATA CHANNEL
206
MASKED 'RANDOM' BIT STREAM
208
CLEAR DATA STREAM
212
'SECURE' DATA STREAM
210

FIG. 6